

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**In re U.S. Patent Application of**

**ICHISE et al.**

## Art Unit 2812

**Application Number: To Be Assigned**

**Filed: Concurrently Herewith**

**For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE  
AND THE METHOD OF PRODUCING THE SAME**

**Attorney Docket No. HITA.0529**

**Honorable Assistant Commissioner  
for Patents  
Washington, D.C. 20231**

## **INFORMATION DISCLOSURE STATEMENT**

The above-referenced application is a Continuation application of U.S. Application No. 10/319,551 filed December 16, 2002, which is a Divisional application of U.S. Application 09/982,842 filed October 22, 2001.

It is understood that the listed references will be considered in the examination of the application and that no separate copies of the same prior art are required to be provided since they were previously cited or transmitted in the foregoing prior application, pursuant to 37 CFR § 1.98(d). Form(s) PTO 1449 is enclosed listing references cited by the Examining Attorney and submitted by applicant in the prior applications.

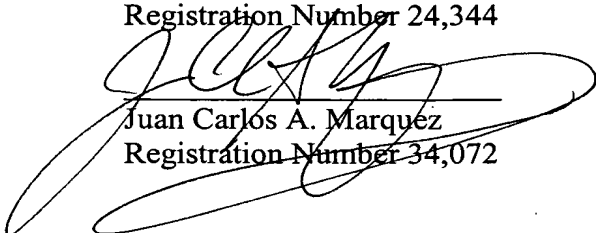
This Information Disclosure Statement is submitted with the above-captioned U.S. Continuation application. Accordingly, no fee is due or payable at this time.

The Examiner is requested to acknowledge consideration of the information provided in this paper in accordance with prescribed procedures.

Please charge any additional fees or credit any overpayments in connection with this paper to Deposit Account No. 08-1480.

Respectfully submitted,

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Form PTO 1449  U.S. Department of Commerce Patent and Trademark Office  Information Disclosure Statement by Applicant	ATTY. DOCKET NUMBER <b>HITA.0529</b>	SERIAL NUMBER To Be Assigned
	APPLICANT <b>ICHISE et al.</b>	
	FILING DATE Concurrently Herewith	GROUP

**U.S. Patent Documents**

Exami ner Initial		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	X	5,604,365	2/18/97	Kajigaya et al.			8/29/94
	X	6,287,914 B1	9/11/2001	Uchiyama et al.			8/17/2000
	X	6,191,444 B1	2/20/2001	Clampitt et al.			9/3/98
	X	5,893,734	4/13/99	Jeng et al.			9/14/98
	XX	6,048,788	4/11/2000	Huang et al.			1/20/98
	XX	5,362,666	11/8/94	Dennison			3/15/94
	XX	6,184,081	2/6/2001	Jeng et al.			10/8/99

**Foreign Patent Documents**

Exami ner Initial		DOCUMENT NUMBER	FILING DATE	COUNTRY	CLASS	SUB- CLASS	TRANSLATION	
							YES	NO
	X	7-122654	8/30/94	Japan				X

**Other Documents (Including Author, Title, Date Pertinent Pages, Etc.)**

EXAMINER	DATE CONSIDERED	
<i>EXAMINER: Initial if citation is considered, whether or not citation is in conformance with MPEP 609; draw a line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant</i>		

X = Filed by Applicant in Parent  
 XX = Cited by Examiner in Parent  
 PTO1449